ENGR-2300

Electronic Instrumentation

Quiz 2

Spring 2023

Print Name _____ RIN _____

Section ____

I have read, understood, and abided by the Collaboration and Academic Dishonesty statement in the course syllabus. The work presented here was solely performed by me.

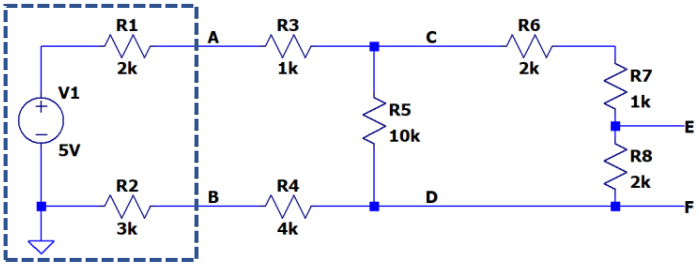
Signature: _____

Date: _____

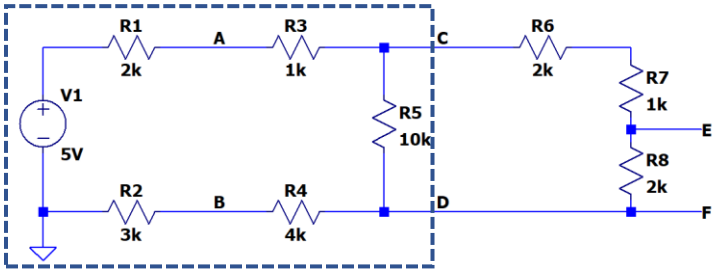
On all questions: SHOW ALL WORK. BEGIN WITH FORMULAS, THEN SUBSTITUTE VALUES AND UNITS. No credit will be given for numbers that appear without justification. Unless otherwise stated in a problem, provide 3 significant digits in answers. Read the entire quiz before answering any questions. Also, it may be easier to answer parts of questions out of order.

I. Thevenin Equivalent Circuits [20 points]

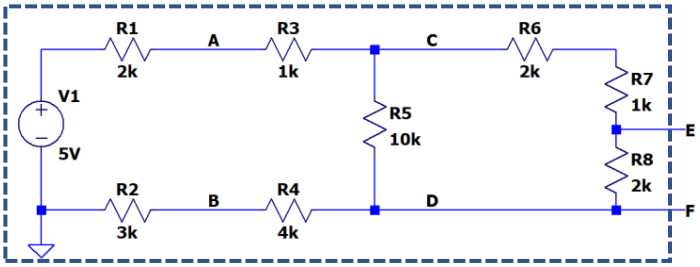
a) [5 pts] Find the Thevenin Equivalent voltage and resistance between terminals A and B (for the portion of the circuit contained in the box). After finding V_{th} and R_{th}, draw the Thevenin Equivalent circuit.



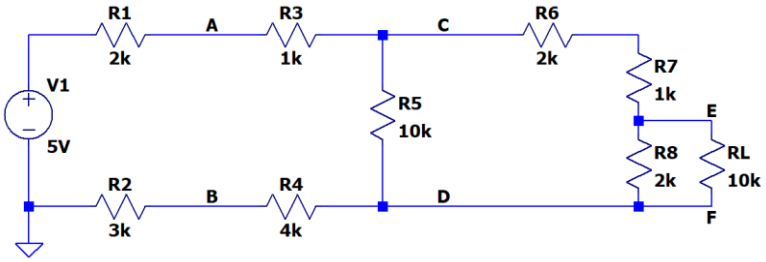
b) [4 pts] Find the Thevenin Equivalent voltage and resistance between terminals C and D (for the portion of the circuit contained in the box). After finding V_{th} and R_{th}, draw the Thevenin Equivalent circuit.



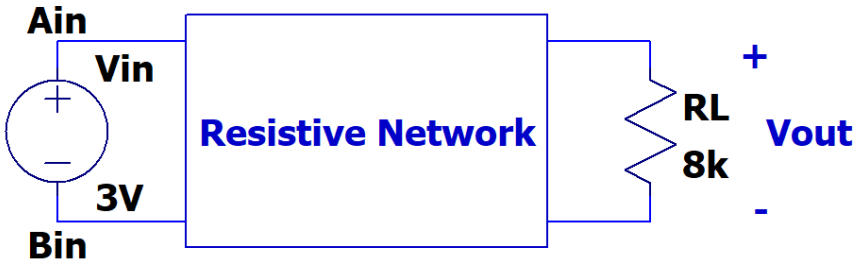
c) [4 pts] Find the Thevenin Equivalent voltage and resistance between terminals E and F (for the portion of the circuit contained in the box). After finding V_{th} and R_{th}, draw the Thevenin Equivalent circuit.



d) [2 pts] If a load resistor with a value RL = 10kΩ is connected across terminals E and F as shown below, how much power is delivered to the resistor?



e) [3 pts] Given an unknown resistive network, you connect a load resistor RL = 8kΩ across the output terminals and apply a voltage Vin = 3V across the input terminals. If you measure a voltage Vout = 2V, what is the equivalent resistance R_{eq} of the resistive network?

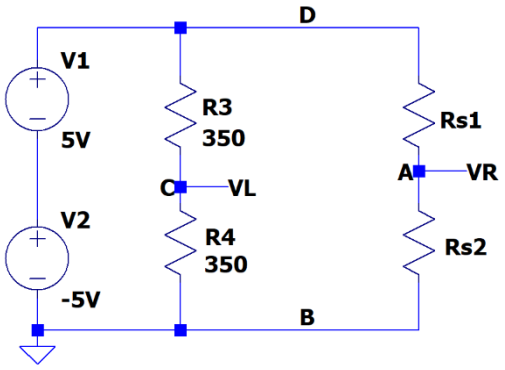


Q1.6) [2 pts] True or False (circle one): only some linear circuits can be expressed as a voltage source in series with an impedance.

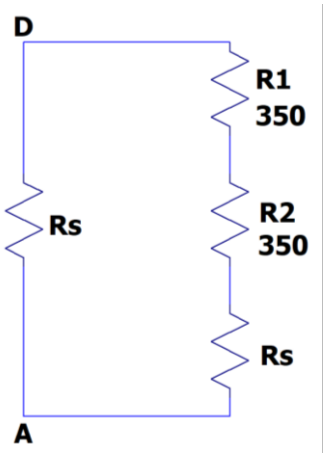
True **False**

II. Bridge Circuits, Strain gauges and Harmonic Oscillation [20 points]

In the circuit below, Rs1 and Rs2 represent the resistances of strain gauges which are attached to the top and bottom of a cantilever beam, as in Experiment 5 and Project 2.



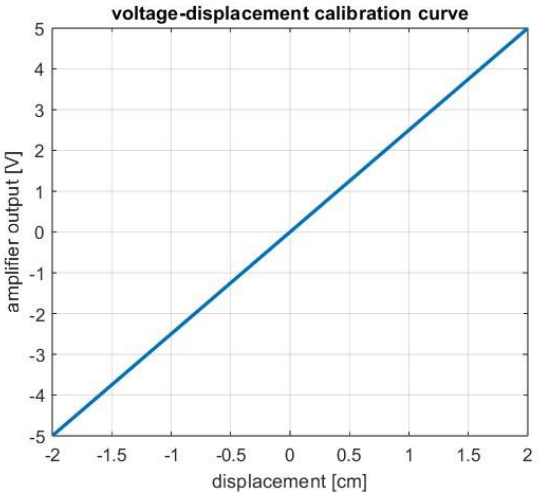
a) [4 pts] When you measured the resistance of the strain gauges in Experiment 5 (for example between terminals D and A), you were measuring Rs1 in parallel with the series combination of R1, R2, and Rs2, as shown below. If both strain gauges are assumed to have the same resistance Rs, what is the value of Rs if you measured a resistance of 263Ω between terminals A and D? Give your answer to the nearest ohm.



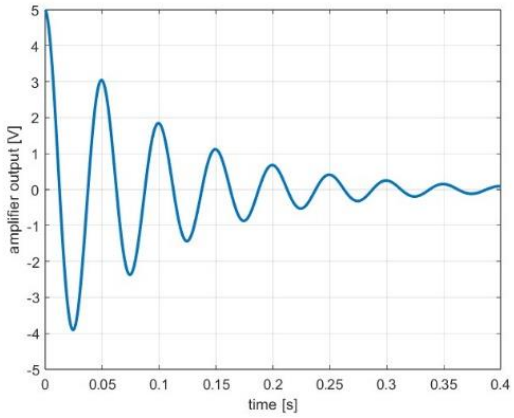
b) [3 pts] When the beam is deflected to its maximum extent, Vout = VL – VR = 10mV. Assuming that the strain gauge resistances become Rs1 = Rs + ΔR and Rs2 = Rs – ΔR, what is the value of ΔR? Give your answer to the nearest 0.1Ω.

c) [2 pts] The output voltage of the strain gauge and bridge circuit is fed into a differential amplifier. Given that Vout = 10mV is the maximum output voltage of the bridge circuit and the op-amp voltage supplies are +5V and -5V, what is the maximum gain of the differential amplifier, such that its output does not saturate?

d) [2 pts] Using the calibration curve below, calculate the proportionality constant that converts the voltage output of the differential amplifier in V to beam displacement in m.



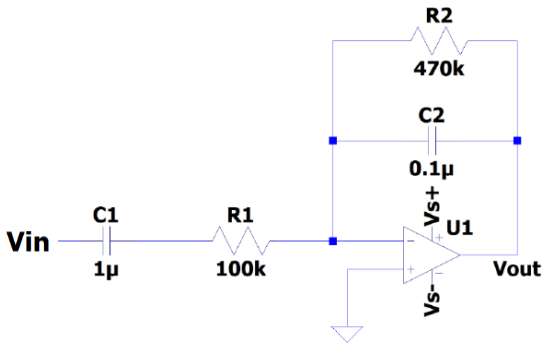
e) [3 pts] Given the curve below, calculate the frequency (in Hz) of the beam oscillation and the decay constant to the nearest 0.1.



f) [2 pts] Which type of op-amp circuit would you use to calculate velocity (up to a constant) from the data in QII.e?

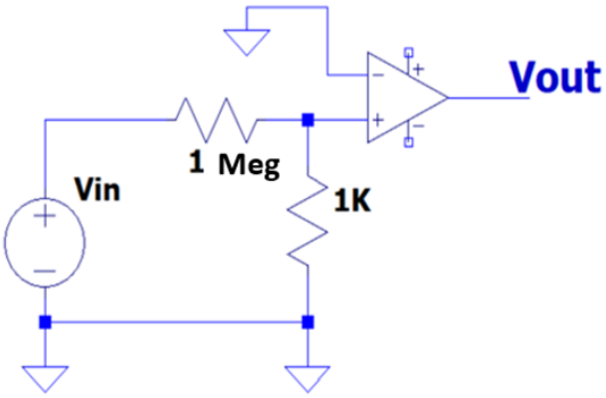
g) [2 pts] If instead you were measuring the voltage signal from an accelerometer, which type of op-amp circuit would you need to calculate velocity (up to a constant)?

h) [2 pts] In the Miller Integrator circuit shown below, what is the function of the capacitor C1 at the input?

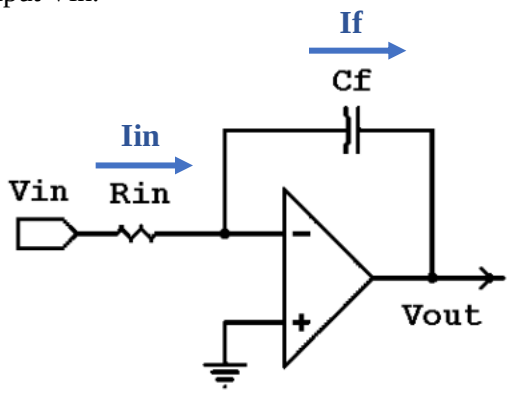


III. Op-Amp Applications and Analysis [26 points]

a) [4 pts] The op-amp circuit below uses an op-amp that is ideal except for having a **finite gain A**. If the measured Vout is 3.5V when Vin is 3.5V, what is the op-amp gain A?
Hint: Vout = A * (V_+ - V_-) when the op-amp is not configured in a feedback loop.



b) [12 pts] In the ideal integrator as shown below, Rin = 100k ohm and the Cf = 0.1uF. A sine-wave signal is applied to the input Vin.



i) [2 pts] Find Iin and If in terms of Vin, Vout, Rin, and the impedance of Cf. Indicate where you used the Golden Rules of op-amps.

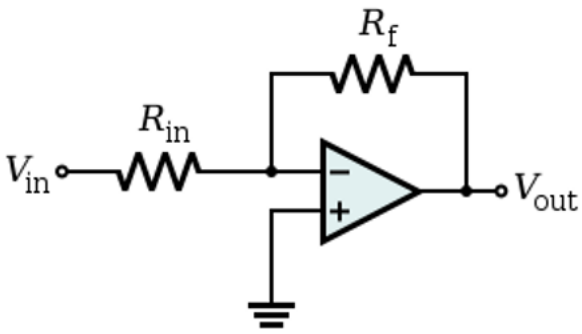
ii) [2 pts] Find the transfer function of this op-amp circuit: $H[j\omega] = Vout/Vin$.

iii) [4 pts] At what frequency (in Hz) are the input and output signal equal in amplitude?

iv) [2 pts] At that frequency, what is the phase of the output signal relative to the input signal?

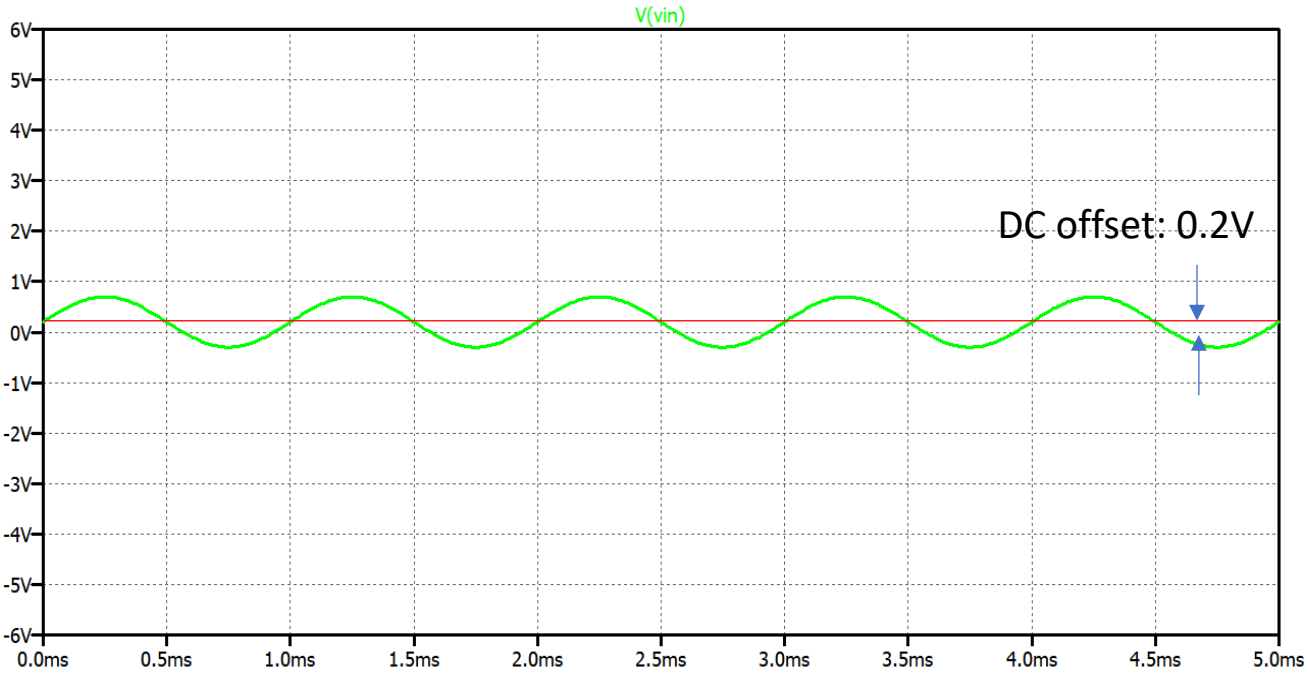
v) [2 pts] If the frequency is lowered by a factor of 10 from that found in iii), by what factor does the output voltage change? In what direction? Choose one: larger or smaller.

c) [10 pts] In the following op-amp application circuit the power supply is Vcc = +/- 5V.

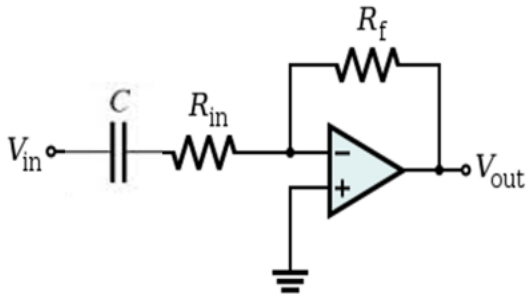


i) [2 pts] If Rin = 10k ohm and we want this amplifier to have a gain of *magnitude*: 10 V/V. What should be the value of the feedback resistor Rf?

ii) [5 pts] If Vin is a sinusoidal wave input of 0.5sin(2π1000t)V on top of DC offset of 0.2V, as drawn below on the V-time plot, draw the output wave form on the graph below. The DC offset is plotted on the graph below as well.

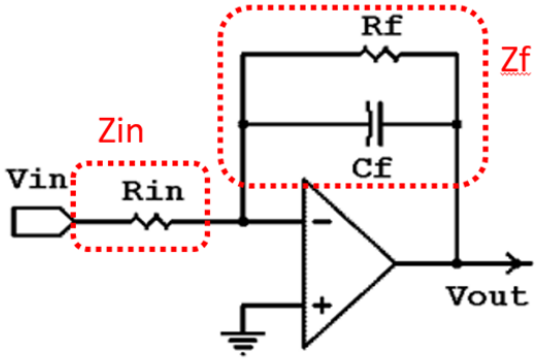


iii) [3 pts] Now we want to remove the effect of the DC offset from the Vin for proper amplification of the input signal. One way is to add a capacitor at the input. What is an appropriate value of the capacitor in the figure below, such that the circuit still operates as an **inverting amplifier**? Hint: what should the relative magnitude of the impedances of Rin and C?



IV. Op-Amp Integrators [14 points]

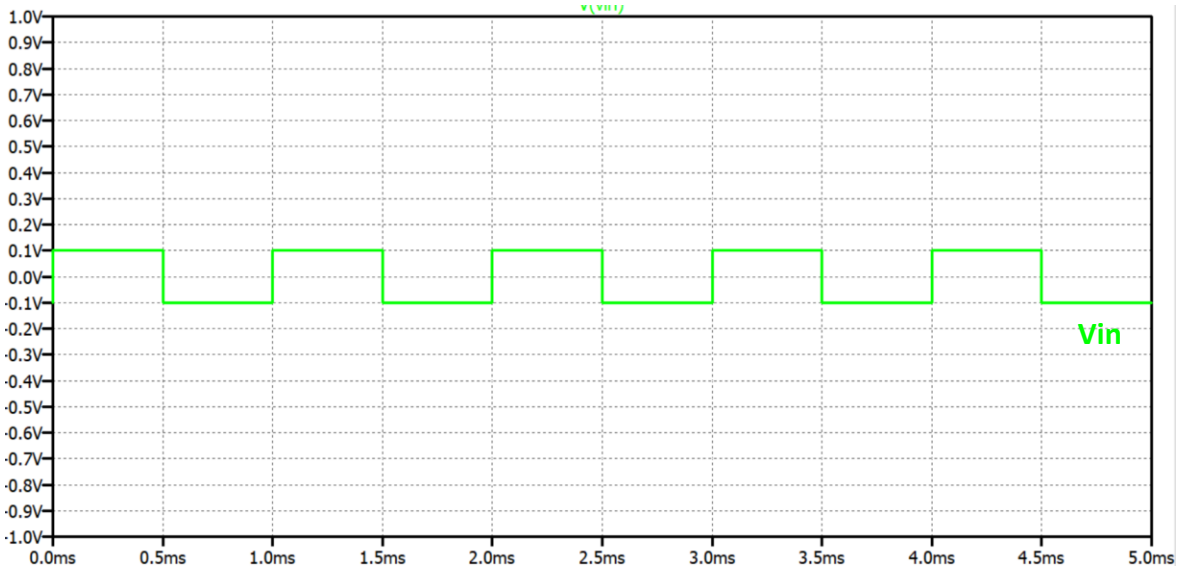
To improve the performance of the ideal integrator in QIII.b, a feedback resistor Rf is added on the feedback path of Cf.



a) [4 pts] Using the same steps as in QIII.b.i and QIII.b.ii, find the transfer function of this op-amp circuit in terms of Rin, Rf and Cf.

b) [3 pts] Given that Rin = 1k ohm, assign values for Rf and Cf such that the DC gain is 100 and the corner frequency (3-dB frequency) is 40Hz.

c) [5 pts] For a 0.1V peak-to-peak square wave input with a frequency of 1kHz, draw the output wave form on the voltage-time plot below. Indicate key values from your calculation (such as min and max) on the plot. Hint: since we are in steady-state the DC offset of the output voltage should be 0.



d) [2 pts] At what frequency does the magnitude of the transfer function reduce to unity (=1)?